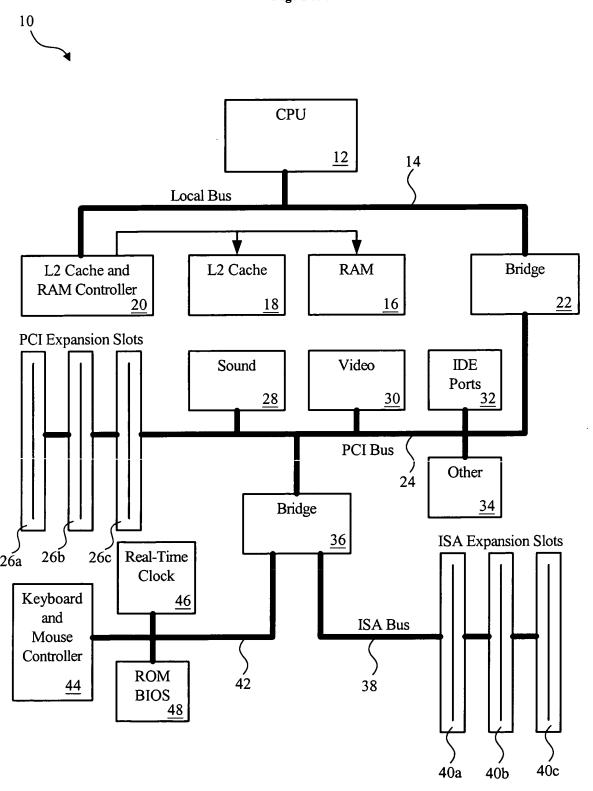
Title: COMPUTER-COMPONENT, POWER-CONSUMPTION MONITORING AND CONTROL Inventor: Hepner et al. Docket No.: 1200.2.80/SJ0920030018US1 (RJB)

Page 1 of 6



Prior Art

FIG. 1

Inventor: Hepner et al.

Docket No.: 1200.2.80/SJ0920030018US1 (RJB)

Page 2 of 6

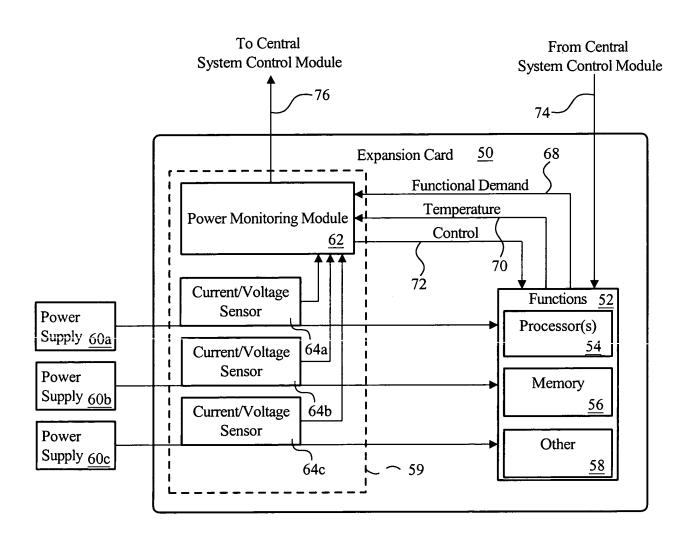


FIG. 2

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Docket No.: 1200.2.80/SJ0920030018US1 (RJB) Page 3 f 6

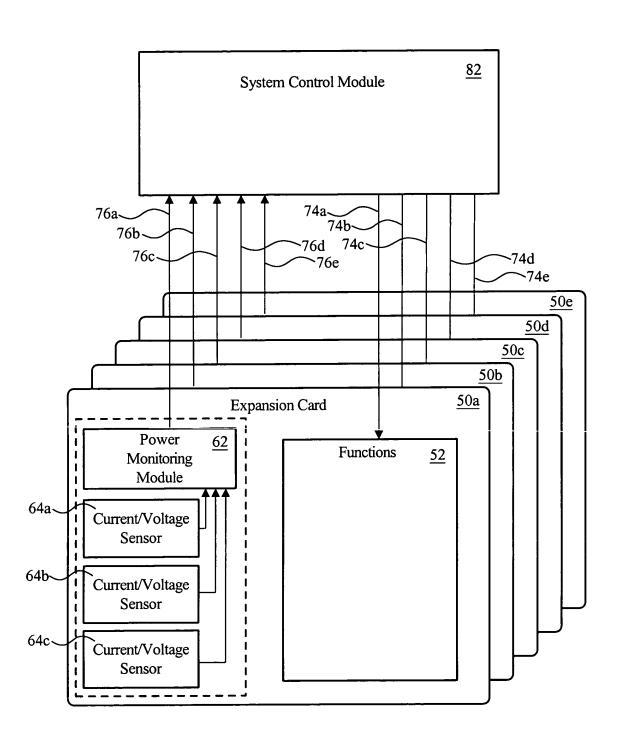


FIG. 3

Title: COMPUTER-COMPONENT, POWER-CONSUMPTION MONITORING AND CONTROL Inventor: Hepner et al.

Docket No.: 1200.2.80/SJ0920030018US1 (RJB) Page 4 of 6

Г	System Conf	trol Module 82
	Monitoring Module 84	Control Module 90
	Functional Demand 94	Action 102
	Operations <u>110</u>	Shutdown 132
	Data Rates 112	Throttle Up <u>134</u>
	Cache Hits 114	Throttle Down 136
	Power Consumption <u>96</u> Current <u>116</u>	Parameter 104 Processor Clock 138
	Voltage 118	Wait States 140
	Leakage Currents 120	Other <u>142</u>
	Temperature 98	Support 92 Software 106
	Counters <u>100</u>	Kemel Extensions 144
	Calculation Module 88	<u>Drivers</u> <u>146</u>
	Number of Devices 122	Embedded Operating System 148
124	Allocated Power per Device	100
126	Maximum Power Available	Hardware 108 Onboard 150
128	Total Power Consumed	Offboard 152
	Safety Factor 130	Piggyback 154

FIG. 4

Title: COMPUTER-COMPONENT, POWER-CONSUMPTION MONITORING AND CONTROL Inventor: Hepner et al.

Docket No.: 1200.2.80/SJ0920030018US1 (RJB) Page 5 of 6

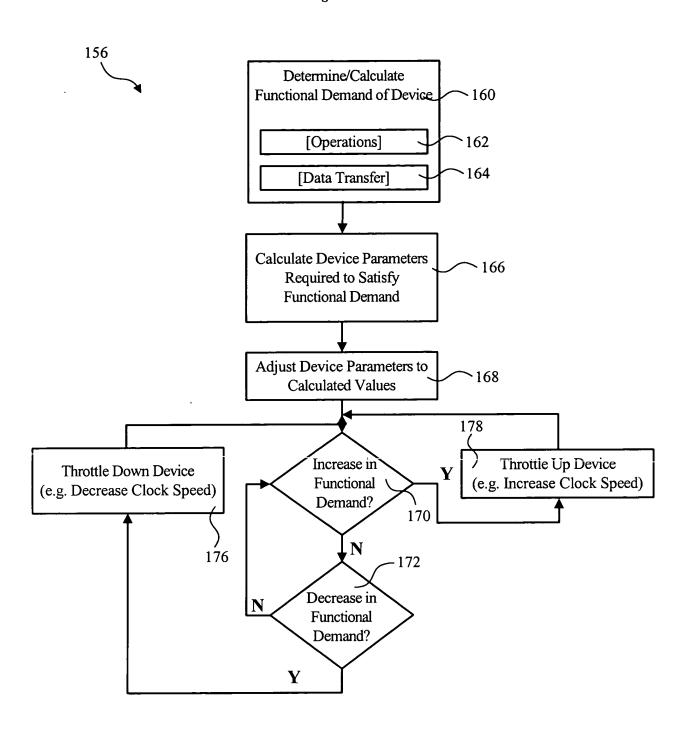


FIG. 5

Page 6 of 6



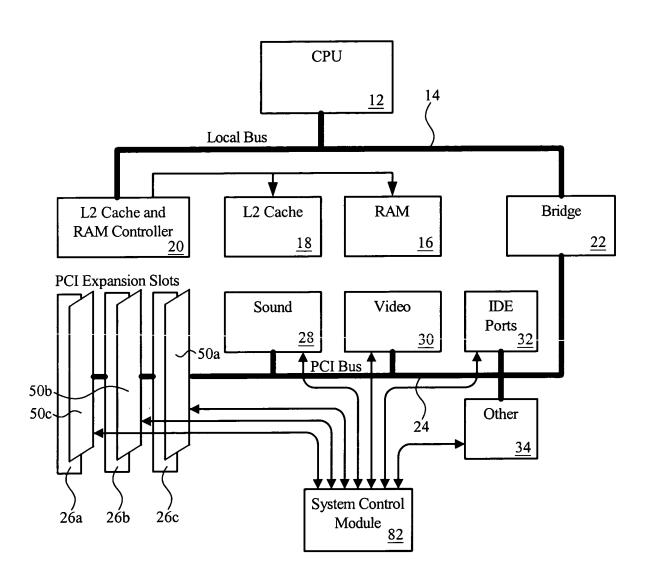


FIG. 6